

AMENDMENTS TO THE SPECIFICATION:

Page 14, replace the paragraph beginning on line 7 with the following amended paragraph:

--The drain of the NMOS transistor 21 is connected with the ~~source~~ drain of the PMOS transistor 41a and the drain of the NMOS transistor 22 is connected with the ~~source~~ drain of the PMOS transistor 43a. The sources of the NMOS transistor 21 and 22 are connected to each other, and connected with the drain of the NMOS transistor 34. The source of the NMOS transistor 34 is connected with the ground line 7. The same input voltage  $V_{IN+}$  as supplied to the gate of the PMOS transistor 11 is supplied to the gate of the NMOS transistor 21. The same inverted input voltage  $V_{IN-}$  as supplied to the gate of the PMOS transistor 12 is supplied to the gate of the PMOS transistor 22. Any one of the NMOS transistor 21 and the NMOS transistor 22 is turned on in response to the input voltage  $V_{IN+}$  and the inverted input voltage  $V_{IN-}$ --